

PV FED INVERTER/CONVERTER CIRCUIT WITH DUAL-MODE CONTROL FOR EV/HEV APPLICATIONS

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Abstract--- In recent years, renewable energy sources such as photovoltaic (PV), wind, fuel cell, etc gain importance due to the limitations of conventional energy sources. Renewable energy sources play an important role in rural areas where the power transmission from conventional energy sources is difficult. Other advantages of renewable energy sources are clean, light and does not pollute atmosphere. Hybrid electric vehicles (HEVs) powered by electric machines and an internal combustion engine (ICE) are a promising mean of reducing emissions and fuel consumption without compromising vehicle functionality and driving performances. The proposed integrated circuit allows the permanent magnet synchronous motor to operate in motor mode or acts as boost inductors of the boost converter, and thereby boosting the output torque coupled to the same transmission system or dc-link voltage of the inverter connected to the output of the integrated circuit. Electric Motors, those are used for EV propulsion must have high efficiency for maximum utilization of the energy from batteries and/or fuel cells. A new control technique for the proposed integrated circuit under boost converter mode is proposed to increase the efficiency. Since the light load performance is in recent focus of interest, appropriate algorithms to improve light load efficiency were implemented. The proposed control technique is to use interleaved control to significantly reduce the current ripple and thereby reducing the losses and thermal stress under heavy-load condition. When pv fed inverter/converter circuit is operated for single input under closed loop, multiple outputs are obtained.

Keywords—RES, hybrid electric vehicles (HEV), boost

converter, electric vehicles (EV), photovoltaic (PV), an internal combustion engine (ICE).

I. INTRODUCTION

In parallel hybrid vehicle and electric vehicle, the converter is used to boost the battery output voltage to rated dc bus and an inverter is used to drive the motor. In multi-motor drive system the system uses two or more motors to boost torque especially under low speed and high torque region. For such applications, two or more converters or inverters are required. Fig.1 shows the application of proposed integrated circuit for EV/HEV applications. The proposed integrated circuit allows the (PMSM) to operate in motor mode or acts as boost inductors of the boost converter, and thereby, boosting the output torque coupled to the same transmission system or dc-link voltage of an inverter connected to the output of the integrated circuit. In motor mode, the proposed integrated circuit acts as an inverter and it becomes a boost-type boost converter, while using the motor windings as the boost inductors to boost the converter output voltage. Therefore, the proposed integrated circuit can significantly reduce the volume and weight of the system.

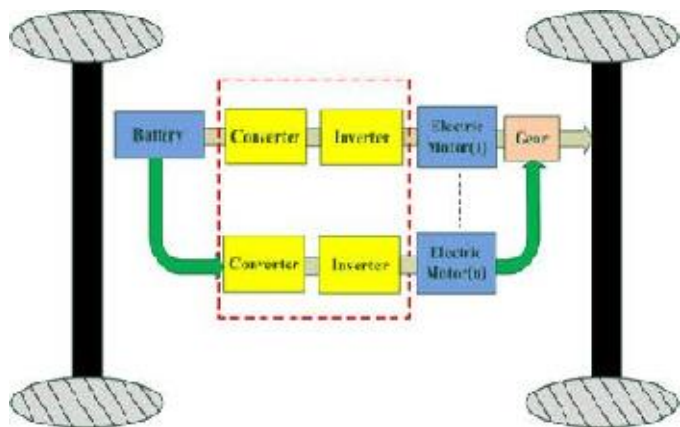


Fig.1. Conventional multi-motor drive system of EV/HEV.

PV technologies are expected to become an attractive power source for automotive applications because of their cleanness, high efficiency, and high reliability. Although there are various PV technologies available for use in automotive systems, many commercial hybrid electric vehicle (HEV) systems use a traditional bidirectional dc-dc converter to interface the battery and the inverter dc bus. There is growing interest in electric vehicle (EV) and hybrid electric vehicle (HEV) technologies because of their reduced fuel usage and greenhouse emissions [1]–[3]. PHEVs have the advantage of a long driving range since fuel provides a secondary resource. Connection to the electric power grid allows opportunities such as ancillary services, reactive power support, tracking the output of renewable energy sources, and load balance. For purposes of this paper, plug-in vehicles will be lumped together with EVs. Most EV charging can take place at home overnight in a garage where the EV can be plugged in to a convenience outlet for Level 1 (slow) charging. Level 2 charging is typically described as the primary method for both private and public facilities and requires a 240 V outlet. However, the electric vehicles remain unpopular among the consumers due to their lack of performance and their inability to travel long distances without being recharged.

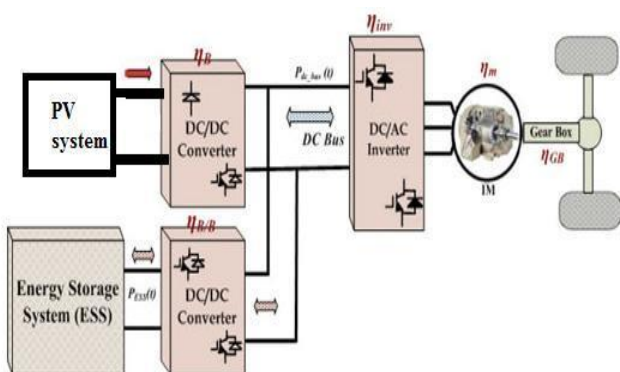


Fig.2 Block diagram of the PV based HEV

II. HEV CONFIGURATIONS

A brief description about various HEV configurations available in the market is presented. The three main configurations are the series, parallel and the dual-mode configurations and the explanation of each one of them with their merits and demerits follows.

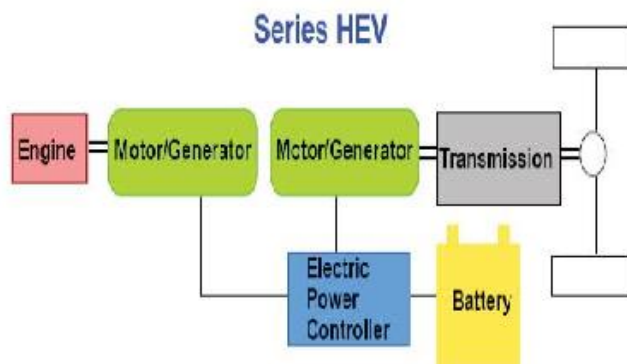


Fig.3 Series HEV drive train

In series HEV configuration, only the electric motor is connected to the drive train and thus the vehicle is entirely driven by the electric motor. The Internal Combustion (IC) engine drives an electric generator (commonly known as alternator), which then supplies the electric power to the motor and battery pack. The IC engine will turn off if the battery is fully charged. In some cases, the electric power supply for the electric motor can come both from the battery and the engine generator set. As only the electric motor is connected to the drive train, the IC engine can run at an optimum speed to run the generator thus greatly reducing the emissions. The batteries can either be charged off-board, by external DC power link from the electric-grid, or on-board, with the help of an alternator and an IC engine. In this setup, it is possible to design the operation such that the IC engine never idles and thus the overall emissions are reduced. The schematics of series HEV is shown in Figure 2.

It can be seen that the IC engine is connected to the alternator (generator) which in turn is connected to the battery pack and electric motor through an electronic control unit. This scheme allows the electric motor to get its power from either battery pack or the alternator or both as per the battery state of charge and vehicle acceleration requirements [4-6].

B. Parallel HEV Configuration

In the parallel HEV configuration there are two power paths for the drive train, while one comes from the engine the other comes from the electric motor. During short trips the electric motor can power the vehicle, while during long drives the IC engine can power the vehicle. The vehicle can thus have engine only, motor only, or a combination of engine and motor mode of operation. the rating of the IC engine can be reduced. The configuration is illustrated in fig 4.

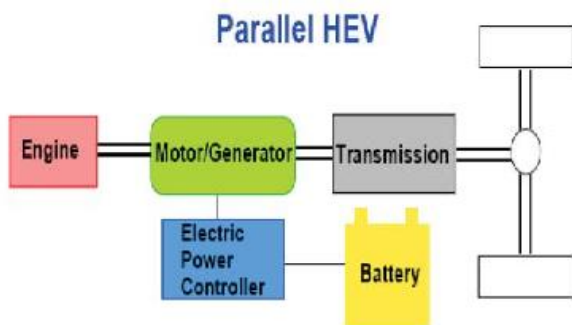


Fig.4 Parallel HEV drive train

In parallel HEV configuration, the drive train is connected to the electric motor and engine through a mechanical coupling or an angle gear. These vehicles do not require a generator (as in the case of series HEV configuration) and they can be connected to an electric grid (off-board) for recharging the batteries. The electric motor can be made to act as generator via a mechanical clutch which can then be used for regenerative braking. Both the gas-powered engine and the electric motor can turn the transmission simultaneously, and the transmission, of course, turns the wheels. The fuel tank and gas engine and the batteries and electric motor connect independently to the transmission—as a result, in a parallel hybrid; both the electric motor and the gas engine can provide power.

III. PROPOSED INTEGRATED CIRCUIT AND CONTROL TECHNIQUE

A. Proposed Integrated Inverter/Converter Circuit

In Fig. 4 C_{in} , and C_{out} can stabilize the voltage when input and output voltages are disturbed by source and load, respectively. Diode (D) is used for preventing output voltage impact on the input side. When the integrated circuit is operated in inverter (motor) mode, relay will be turned ON and six power devices are controlled by pulse width modulation (PWM) control signals.

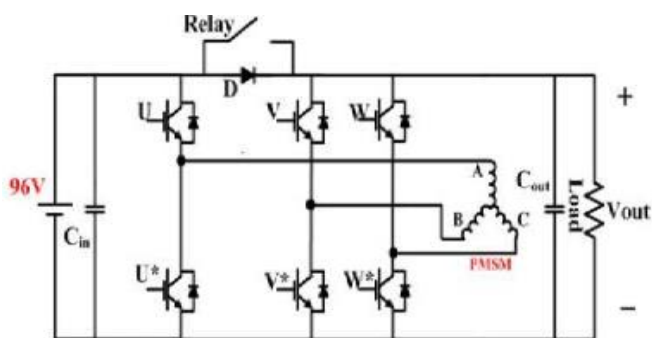


Fig 5. Integrated circuit for dual mode control

When the proposed integrated circuit is operated in the

converter mode, relay is turned OFF. And a single-phase or interleaved control method will be applied to control of the power devices depending upon the load conditions.

B. Modeling and Controller Design under Boost Mode

This section will introduce the model of boost converter and derive the transfer function of the voltage controller. Fig.5 shows the non ideal equivalent circuit of the boost converter, it considers non ideal condition of components: inductor winding resistance RL , collector-emitter saturation voltage V_{CE} , diode forward voltage drop VD , and equivalent series resistance of capacitor R_{esr} .

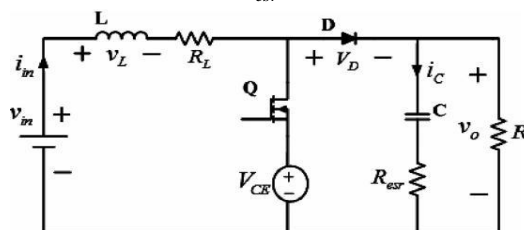


Fig.6 Equivalent circuit of the boost converter

Analysis of the boost converter by using the state-space averaging method [4], small-signal ac equivalent circuit can be derived, as shown in Fig. 6.

$$G_{vd}(s) = \frac{-6.737 \times 10^{-5} s^2 + 0.06827s + 2498}{2.004 \times 10^{-5} s^2 + 0.00409s + 3.242} \dots(1)$$

Fig7. shows the block diagram of voltage loop, using a proportional-integral (PI) controller for the compensator. In this paper, the switching frequency is 20 kHz and voltage loop bandwidth will be less than 2 kHz. And the phase margin should be more than 45° to enhance the noise immunity. For the designed controller shown.

$$C(s) = \frac{0.0248387s + 13.073}{s} \dots(2)$$

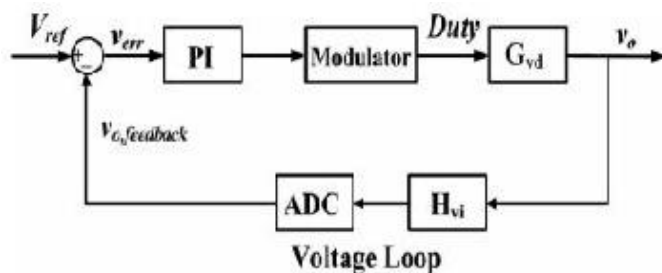


Fig.7 Block diagram of voltage loop

C. Proposed Control Technique of the Integrated Inverter/Converter Circuit

Fig. 9 shows the measured efficiency of the proposed integrated circuit and switching point of different voltage ratios as it acts as a boost converter. As shown in Fig. 12(a), the efficiency for

interleaved control is increased as load goes more than 2.4 kW as compared to that for single-phase control. There-fore, the boost converter is controlled by the proposed hybrid control method shown in Fig. 13. As shown in Fig. 13, as load is more than the switching point of power ratio for the given voltage ratio shown in Fig. 12(b), the converter is controlled by interleaved mode to significantly reduce the current ripple and thereby reducing the losses. In contrast, as load is less than the switching point of power ratio for the given voltage ratio shown in Fig. 12 (b), the converter is controlled by the single-phase control method without invoking additional conduction and switching losses as compared to that for two-phase inter-leaved control. The transition point is determined by the load condition and implemented in the interrupt service routine (ISR) as shown in Fig. 13 for the flow chart of the proposed control for the proposed integrated circuit under boost converter mode.

IV. PHOTOVOLTAIC (PV) SYSTEM

The power that one module can produce is seldom enough to meet requirements of a home or a business, so the modules are linked together to form an *array*. Most PV arrays use an inverter to convert the DC power produced by the modules into alternating current that can power lights, motors, and other loads. The modules in a PV array are usually first connected in series to obtain the desired voltage; the individual strings are then connected in parallel to allow the system to produce more current. The PV array is made up of number of PV modules connected in series called string and number of such strings connected in parallel to achieve desired voltage and current. The PV module used for simulation study consists of 36 series connected polycrystalline cells. In the crystalline silicon PV module, the complex physics of the PV cell can be represented by the equivalent electrical circuit shown in below figure. For that equivalent circuit, a set of equations have been derived, based on standard theory, which allows the operation of a single solar cell to be simulated using data from manufacturers or field experiments.

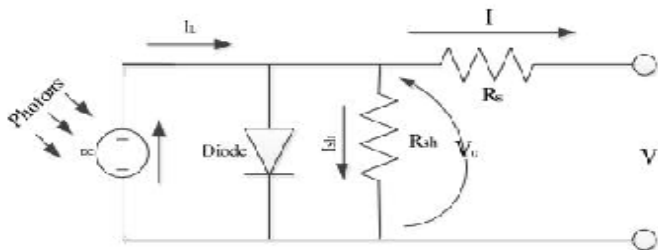


Fig.8 Equivalent electrical circuit of a PV module

The series resistance R_s represents the internal losses due to the current flow. Shunt resistance R_{sh} , in parallel with diode, this corresponds to the leakage current to the ground. The single exponential equation which models a PV cell is extracted from the physics of the PN junction and is widely agreed as echoing the behavior of the PV cell. The number of PV modules connected in parallel and series in PV array are

used in expression. The V_t is also defined in terms of the ideality factor of PN junction (n), Boltzmann's constant (K_B), temperature of photovoltaic array (T), and the electron charge (q). Applied a dynamical electrical array reconfiguration (EAR) strategy on the photovoltaic (PV) generator of a grid-connected PV system based on a plant-oriented configuration, in order to improve its energy production when the operating conditions of the solar panels are different. The EAR strategy is carried out by inserting a controllable switching matrix between the PV generator and the central inverter, which allows the electrical reconnection of the available PV modules.

V. SIMULATION RESULTS

Here the simulation carried by two different cases. They are 1)Proposed interleaved boost converter 2) PV as input source of proposed converter with interleaved boost converter.

Case-1Proposed interleaved boost converter

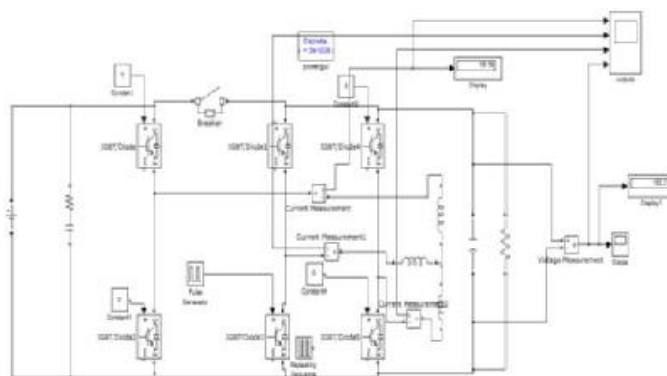


Fig 9: Matlab/simulink model of the integrated circuit and controller

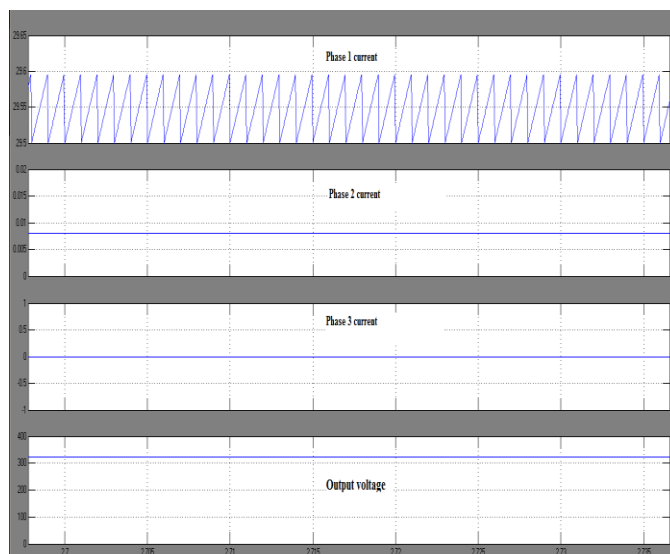


Fig.10 measured current and voltage with and without interleaved control, Single-phase interleaved boost converter

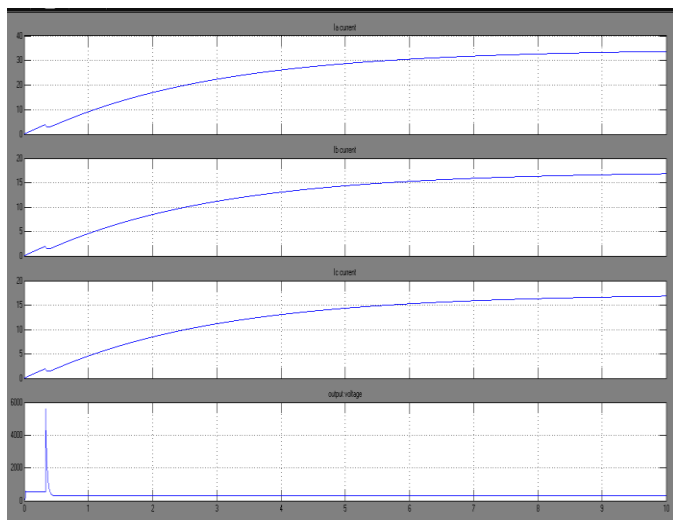


Fig 11: Measured current with interleaved control, Two-phase boost Converter

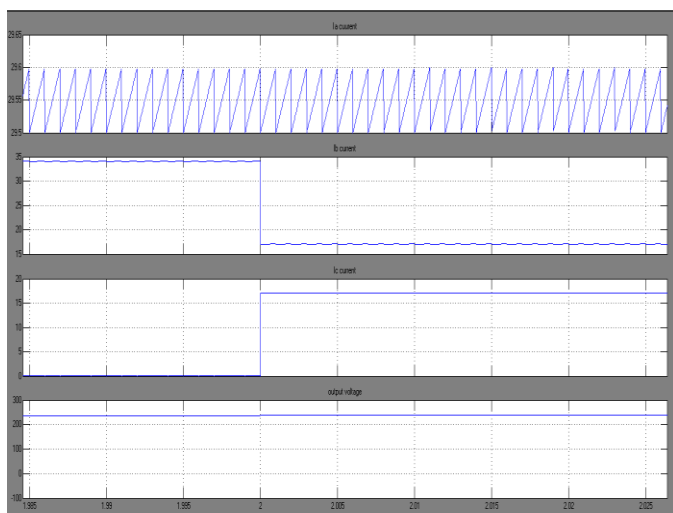


Fig.12 simulated waveforms for the transition between single-phase control and two-phase interleaved control from two-phase

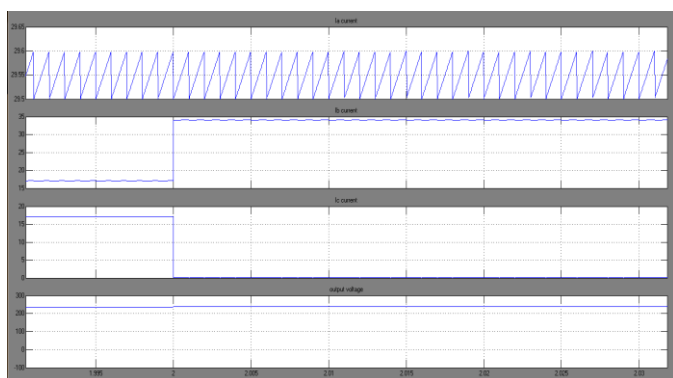


Fig. 13 simulated waveforms for the transition between single-phase control and two-phase interleaved control single-phase to two-phase interleaved modes

Case-2 proposed interleaved boost converter with PV source

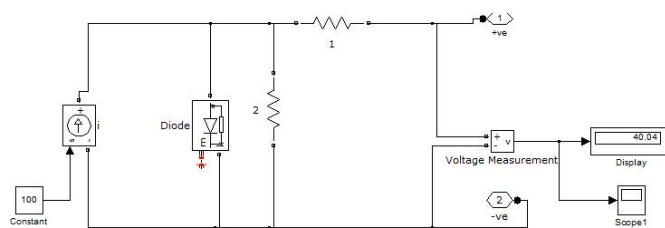


Fig 14: simulation model of PV system

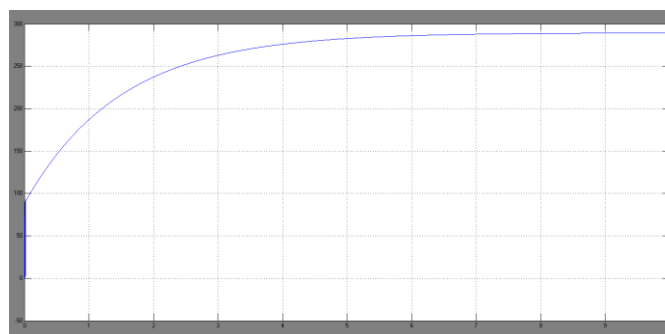


Fig 15: Measured voltage under closed loop

VI. CONCLUSION

When the proposed integrated circuit operates in single-phase mode, the inductor current flows through the power device V^* and its temperature will go up to 87.9°C as shown in Fig. 19(a). In contrast, when the proposed integrated circuit operates in two-phase interleaved mode, the inductor current flows through the power devices V^* and W^* and their temperature will be 62°C as shown in Fig. 19 (b). This comparison result confirms the merit of the control method for the integrated circuit under the boost-mode operation. An HEV intelligently gets around the individual problems associated with the gasoline engine and the electric vehicle. It diminishes the production of emissions and the use of fuel. The problem of batteries for the electric vehicle is conquered. An HEV charges itself – it never has to be plugged in. When not in use providing power, the motor can run as a generator to transfer energy from regenerative braking and from the gasoline engine to the batteries. The only recharging necessary is refueling by going to the gas station. Also, there is not the same demand on the batteries as there would be in an electric vehicle, where the batteries must store all the energy the car needs. Proposal of a new integrated inverter/converter circuit of motor drives with dual-mode control for EV/HEV applications to significantly reduce the volume and weight, proposal of a new control method for the integrated inverter/converter circuit operating in boost converter mode to increase the efficiency, verification of the proposed integrated inverter/converter circuit. The above proposed converter tested under closed loop and multiple output voltages for single input is verified.

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