

Asymmetrical PWM Full-Bridge Converter for Renewable Energy Sources.

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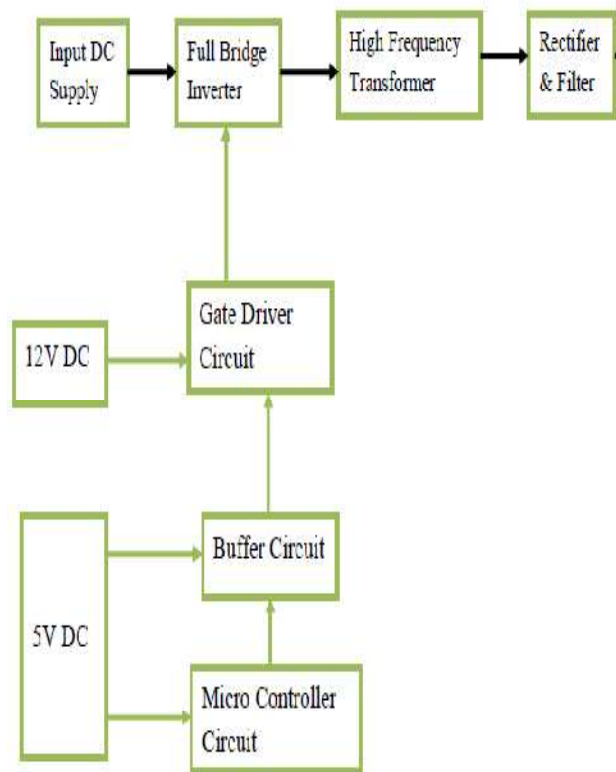
Abstract—This paper provides a particularly green asymmetrical pulse-width modulated (APWM) full-bridge converter for renewable power resources. The proposed converter adopts full-bridge topology and uneven manage scheme to reap the 0-voltage switching (ZVS) turn-on of the strength switches of the primary facet and to lessen the circulating contemporary loss. Furthermore, the resonant circuit composed of the leakage inductance of the transformer and the blocking off capacitor gives the zero contemporary switching (ZCS) flip-off for the output diode without the assist of any auxiliary circuits. Accordingly, the opposite recuperation trouble of the output diode is removed. Similarly, voltage stresses of the strength switches are clamped to the input voltage. Due to those traits, the proposed converter has the structure to decrease energy losses. Its miles in particular useful to the renewable strength conversion systems. To verify the theoretical analysis and validity of the proposed converter, a 400W prototype is carried out with the input voltage range from 40V to 80V.

Index Terms— full-bridge converter, tender switching, say- metrical PWM

I INTRODUCTION

With the exhaustion of the worldwide resources and the environmental pollutants, the research at the renewable power sources including gas cells and photovoltaic cells has been regularly extended in business fields [1]-[4]. Generally, the renewable power sources generate low-voltage strength. The photovoltaic cells which rely upon environment conditions particularly generate fluctuating low-voltage

electricity. Hence, a front-raise converter for fluctuating low voltage electricity is required between the low voltage supply and cargo requiring excessive voltage as proven in Fig. 1. The strength potential of these the front-stop converter is typically less than 250W. As the cellular technology advances, the energy ability of front-give up converter should be elevated. It may also reduce the price in step with watt due to the multiplied power capability. As a result, the expanded strength rating of the front-stop converter is needed to cope with massive electricity rating of the advanced cells and decrease the price consistent with watt of the renewable electricity machine [5]. Some of the front-raise converters, forward/fly back converters that use an active-clamp with voltage double, LLC converters, and segment-shift full-bridge (PSFB) converters are usually not unusual topologies considered for growing electricity capability [6]. An energetic-clamp circuit correctly realizes the zero-voltage switching (ZVS) for the switches through using the leakage inductance, the magnetizing inductance, and the parasitic capacitance.



II. EVALUATION OF APWM COMPLETE BRIDGE CONVERTER

A. Circuit Configuration and Operation principle

With the exhaustion of the worldwide sources and the environmental pollution, the research at the renewable power assets which includes fuel cells and photovoltaic cells has been regularly prolonged in commercial enterprise fields [1]-[4]. Usually, the renewable strength resources generate low-voltage electricity. The photovoltaic cells which rely upon surroundings conditions mainly generate fluctuating low-voltage energy. As a result, a front-end converter for fluctuating low-voltage strength is required among the low voltage supply and load requiring excessive voltage as demonstrated in Fig. 1. The power potential of these the front-stop converters is usually much less than 250W. Because the mobile generation advances, the strength capacity of front-give up converter

ought to be accelerated. It could additionally lessen the price in keeping with watt due to the elevated strength capability. As an end result, the extended energy score of

the front-stop converter is needed to cope with huge electricity score of the superior cells and decrease the fee regular with watt of the renewable electricity device [5]. Some of the front-quit

converters, forward/fly back converters that use an lively-clamp with voltage double, LLC converters, and segment-shift full-bridge (PSFB) converters are commonly not unusual topologies taken into consideration for developing energy capability [6]. A lively-clamp circuit efficiently realizes the 0-voltage switching (ZVS) for the switches via the usage of the leakage inductance, the magnetizing inductance, and the parasitic capacitance. Mainly, forward/fly back converter that use the lively-clamp with voltage double provide the 0-modern-day-day switching (ZCS) of the diodes of the transformer secondary aspect because of the resonant-shaped with the leakage inductance and the resonant capacitor. However, in advance/ fly back converters have a far better voltage strain across the

primary switches of the transformer than the enter voltage. Consequently, the MOSFET with low on resistance $R_{DS(on)}$ cannot be employed. With variable frequency manage, LLC resonant converter can be employed in all packages with variable enter and output voltages, call for of high overall performance and power density. However, due to very extensive bandwidth, the frequency have to be elevated very excessive to acquire enough voltage advantage controllability. Specifically, conventional LLC resonant topology because the front-quit converter of the micro-inverter is hardly ever implemented because it is tough to keep moderate performance over fluctuating input voltage with distinct load situations. The phase-shift complete-bridge (PSFB) converters are extensively used for high

performance in the medium energy packages. Because of the truth its systems are simple and switches are operated with smooth switching without greater components.

III. EXPERIMENTAL RESULTS

To affirm the validity of the proposed APWM complete-bridge converter, a 400W prototype as shown in Fig. 7 became implemented and examined the usage of a DSP processor, microchip dsPIC33EP512GM604. The 400W prototype is a kind of software among low input voltage variety and load which calls for better voltage. All parameters of the prototype are correctly designed to gain the extraordinarily efficient underneath low input voltage range. On this segment, design considerations of the proposed APWM complete-bridge converter are discussed for its excessive performance operation with tender switching approach, and experimental waveforms constitute smooth switching of electricity switches and output diode. Further, the measured electricity performance are offered in line with the enter voltage and the output electricity.

A. Design Issues

The output voltage and the maximum energy of the APWM full-bridge converter are specific as $V_O=350V$ and $P_o=400W$ ($R_{load}=306\Omega$). The duty ratio is selected to be the maximum obligation D_{ax} to cover the most output electricity at the minimal enter voltage. Then, the flip ratio of the transformer is selected from (20) as $n=8$ ($U_{pins}=6$: forty eight). From ZVS flip on condition (21) of the switches S1 and S4, the magnetizing inductance L_m must be much less than $43\mu H$ to assure the ZVS operation of the electricity switches. The better magnetizing inductance induce the decrease root mean square values of the number one and secondary current, which reduce the conduction loss. But, the

transformer saturation have to be taken into consideration in the operating frequency. Accordingly, the magnetizing inductance L_m is practically decided on as $28\mu H$ and the leakage inductance L_{lk} is measured as zero. $45\mu H$. $C_{ob} = 7.6\mu F$ can be decided on.

B. Experimental waveforms and performance

According to the input voltage 40V and 80V, Fig. eight and Fig 9 illustrate the experimental waveforms of the present day i_{S1} and i_{S2} and the voltage v_{S1} and v_{S2} across S1 and S2 at complete load. Whilst the switches S1 and S2 are turned on, the present day i_{S1} and i_{S2} go with the flow via the anti-parallel diode of each switch. As a result, all power switches gain ZVS in the interim of the flip-on, and the voltages v_{S1} , v_{S2} are clamped to the enter voltage V_{dd} . As shown in Fig. 10, the output current i_o is zero before the output diode D_o is became off. As a consequence, the losses because of the reverse-recovery problem are absolutely eliminated. To reveal the strength-loss breakdown, the calculated loss distributions of the foremost additives in desk II are represented.

IV. CONCLUSION

On this paper, APWM full-bridge converter for the renewable power conversion system that may range between the input voltage 40V and 80V has been proposed beneath ZVS and output diode operates under ZCS without extra components. Additionally, all energy switches are clamped to the enter voltage. Accordingly, the proposed converter has the shape to reduce electricity losses. Those advantages make the proposed converter appropriate for fluctuating input voltage on renewable power conversion structures. The prototype of the APWM full-bridge converter are furnished to validate the proposed concept. Most efficiency of ninety six.8% is acquired on the enter voltage 50V and the rated strength 400W.

REFERENCES

- [1] Md. Faro Ali and Refuel Zamani Khan. "The Study on Load Balancing Strategies in Distributed Computing System". International Journal of Computer Science & Engineering Survey (IJCSSES) Vol.3, No.2, April 2012.
- [2] Penmasta, S. and Chronopoulos, A. T. (2007), Dynamic Multi-User Load Balancing in Distributed Systems, IEEE International Parallel and Distributed Processing Symposium, 122K. Elissa, "Title of paper if known," unpublished.
- [3] Sharma S., Singh S., and Sharma M. (2008), Performance Analysis of Load Balancing Algorithms, Proceedings of World Academy of Science, Engineering, and Technology, 28, 269-272
- [4] B. Santosh Kumar and Dr. Latha Parthiban², "An Implementation of Load Balancing Policy for Virtual Machines Associated with a Data Centre", International Journal of Computer Science & Engineering Technology (IJCSSET), volume 5 no. 03, March 2014, p253- 261.
- [5] Nathanael Cherié, Erik Saule "Considerations on Distributed Load Balancing for Fully Heterogeneous Machines: Two Particular Cases" 2015 IEEE International Parallel and Distributed Processing Symposium Workshop IEEE DOI 10.1109/IPDPSW.2015.366
- [6] Shriya and Pradhan, "Review of load balancing in cloud computing". International Journal of Computer Science, vol. 10 issue. 1, Jan 2013.
- [7] N. S. Madhava and D. Singh, "Comparative Study on Load Balancing Techniques in Cloud Computing," vol.1, no. 1, pp.18-25, 2014.
- [8] L.K.Dey, Ghosh, Saaty Bagchi "Efficient Load Balancing Algorithm Using Complete Graph W.Chang(Ed): ICAIC 2011, Part V, CCIS 228 pp: 643-646, 2011.
- [9] Deepak, Divya Wadhwa, Nitin Kumar, "Performance Analysis of Load Balancing algorithms in Distributed Systems" Advance in Electronic and Electric Engineering. ISSN 2231-1297, Volume 4, Number 1 (2014), pp. 59-66.
- [10] Daniel Grouse, Anthony T., "Non-Cooperative load balancing in distributed systems". Journal of Parallel and Distributed Computing, 2005.